

O I E BY JC
 DRAFTSMAN
 MAR 05 2003
 PATENT & TRADEMARK OFFICE

APPROVED
 CLASS SUBCLASS

100 ↗

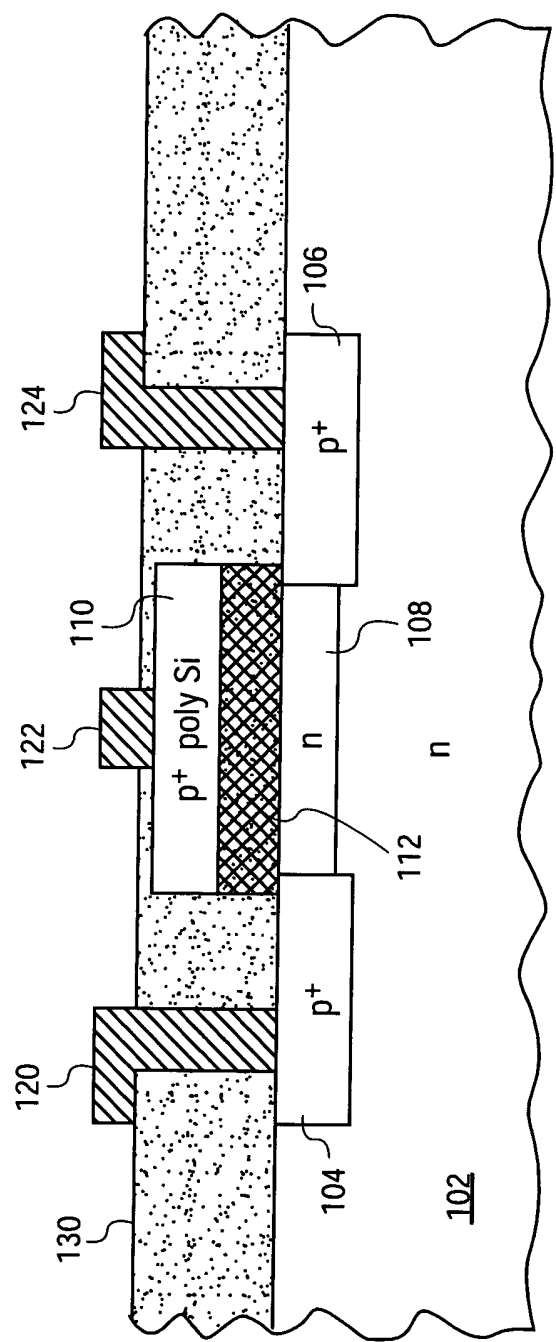
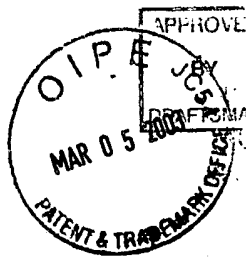


FIG. 1



APPROVED	CLASS	SUBCLASS

200

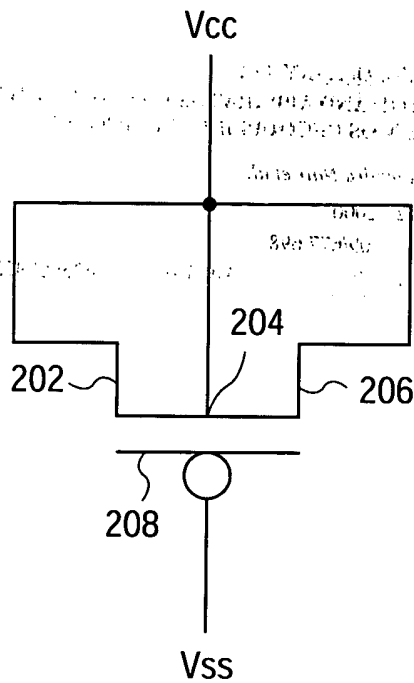


FIG. 2

300

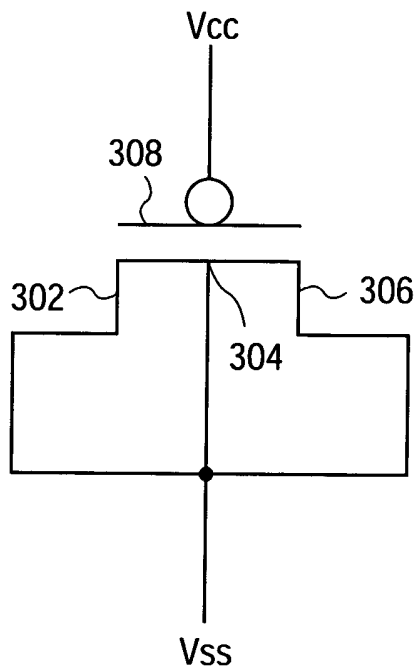
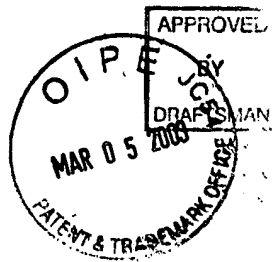


FIG. 3



APPROVED	O.G. FIG.
CLASS	SUBCLASS

FIG. 4 is a graph showing the normalized value of C as a function of V. The curve is labeled 410 and 420.

FIG. 4 is a graph showing the normalized value of C as a function of V.

FIG. 4 is a graph showing the normalized value of C as a function of V.

FIG. 4 is a graph showing the normalized value of C as a function of V.

FIG. 4 is a graph showing the normalized value of C as a function of V.

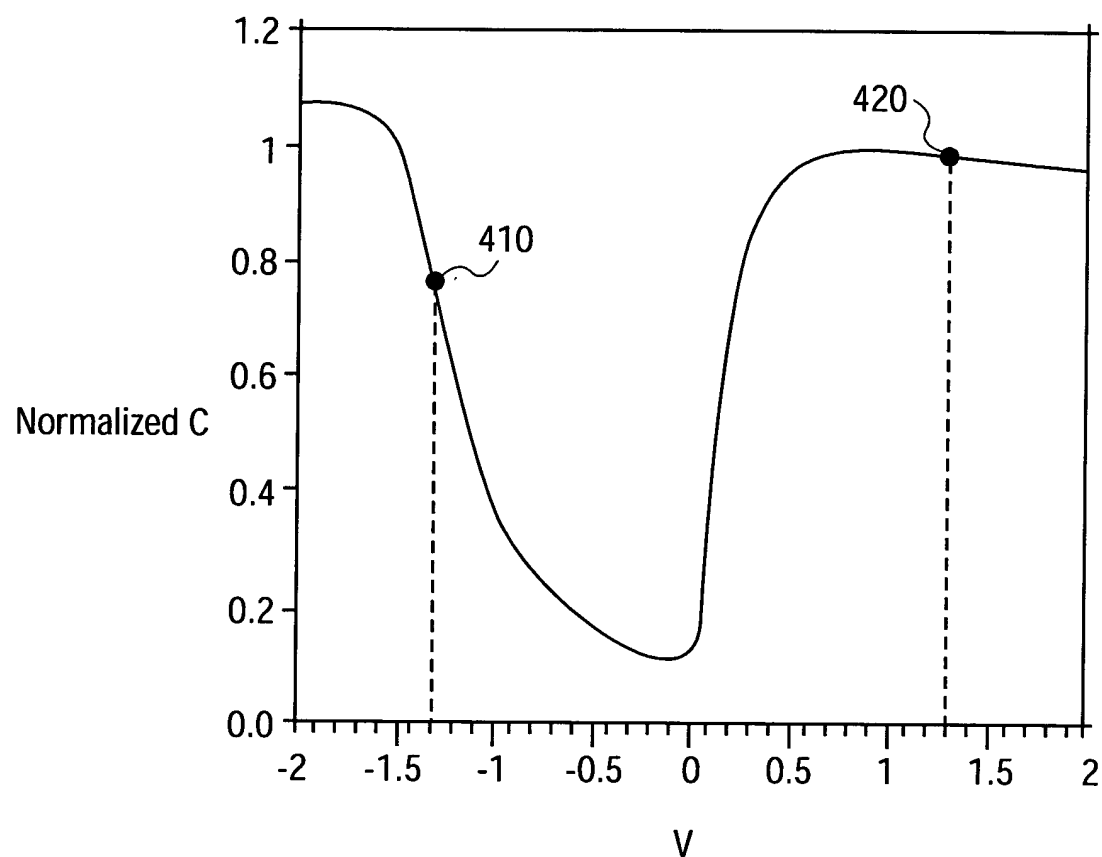


FIG. 4

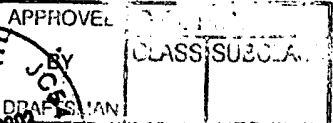
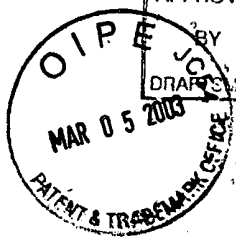


FIG. 5



APPROVED	BY	CLASS	SUBCLASS
	DRAFTSMAN		

FIG. 6 is a schematic diagram of a circuit 600. The circuit 600 includes a PMOS transistor 602 and an NMOS transistor 604. The gates of the PMOS transistor 602 and the NMOS transistor 604 are connected to a common gate node 606. The source of the PMOS transistor 602 is connected to a supply voltage Vcc, and the source of the NMOS transistor 604 is connected to a supply voltage Vss. The drains of the PMOS transistor 602 and the NMOS transistor 604 are connected to a common drain node 608. The circuit 600 is configured as a CMOS inverter.

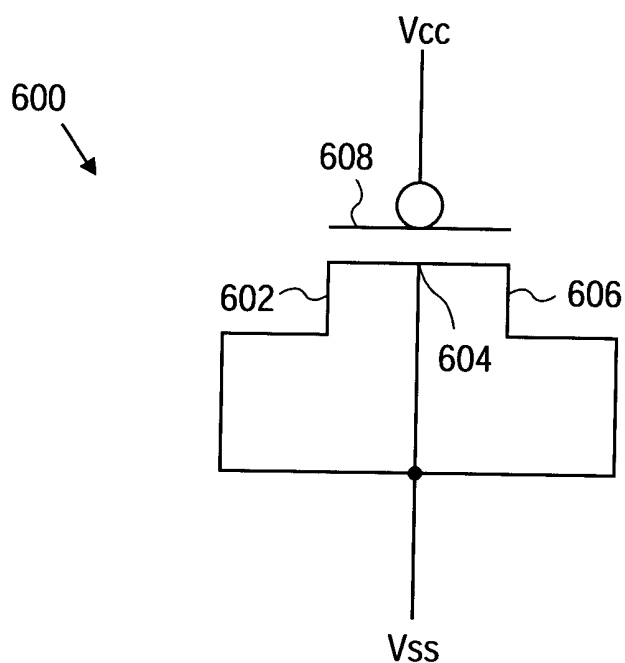
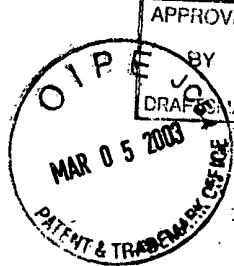


FIG. 6



APPROVED	NO. FIG.
BY	CLASS
DRAFTSMAN	SUBCLASS

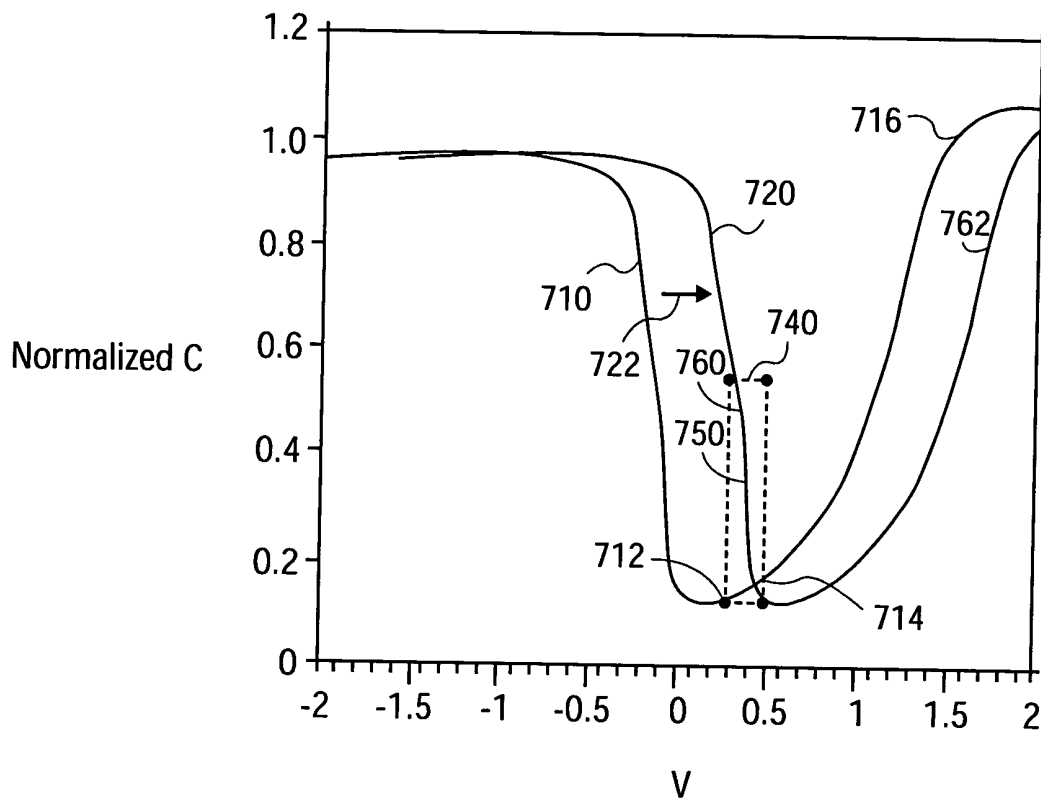


FIG. 7

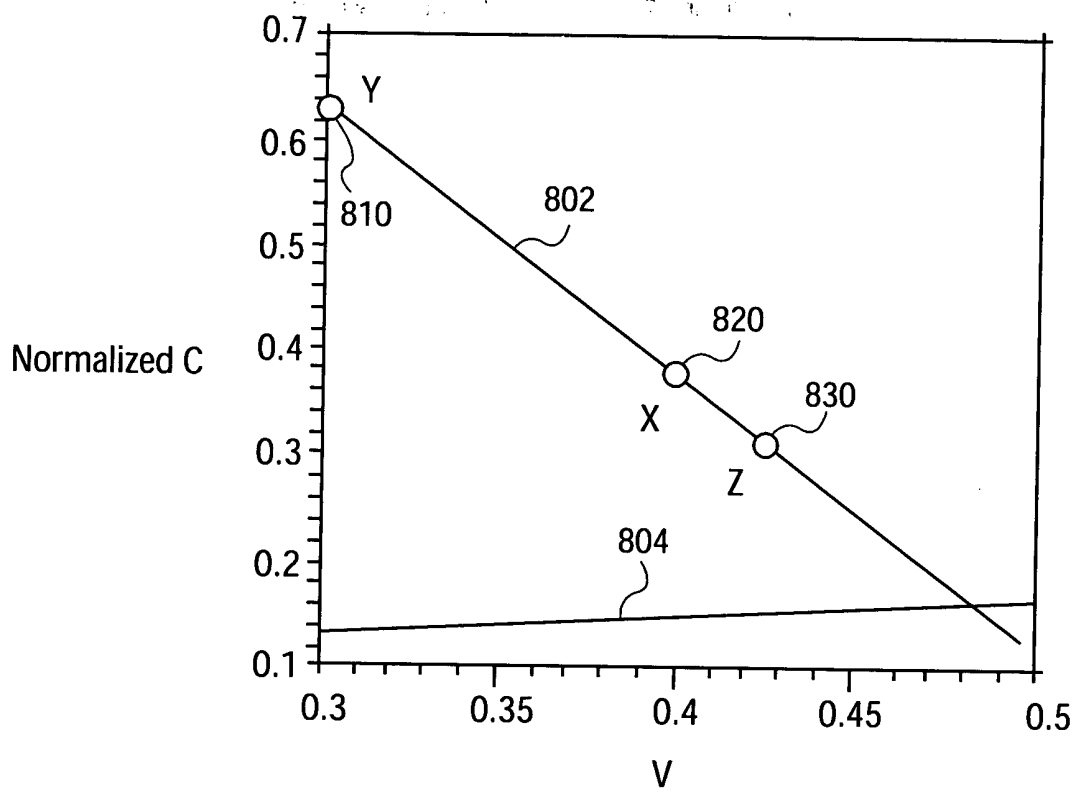
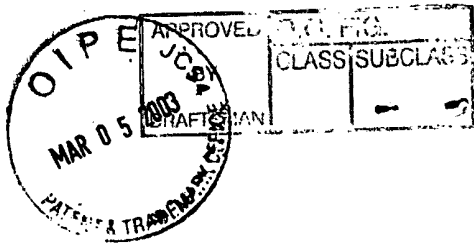


FIG. 8